



INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Art Unit	2128 2117
Examiner Name	Phung M. Chung

U.S. PATENT DOCUMENTS

Copies of U.S. Patent documents do not need to be provided, unless requested by the Patent and Trademark Office. For patents, provide the patent number and the issue date. For published U.S. applications, provide the publication number and the publication date. For unpublished pending patent applications, provide the application number and the filing date.

Examiner's Initials*	Cite No. (optional)	Number	Publication Date	Name of Applicant or Patentee
/PC/		5,072,178	12/10/1991	Matsumoto
/PC/		5,642,362	6/24/1997	Savir
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Examiner's Initials*	Cite No. (optional)	Country	Number	Publication Date	Name of Applicant or Patentee
/PC/		Japan	63-286780 (w/partial translation)	11/24/1988	Iwasaki et al.
/PC/		Japan	03-012573 (w/English abstract)	1/21/1991	Matsumoto
/PC/		Japan	05-249197 (w/English abstract)	9/28/1993	Ikenaga et al.
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OTHER DOCUMENTS

Examiner's Initials*	Cite No. (optional)	
/PC/		Konemann et al., "Built-In Logic Block Observation Techniques," <i>IEEE Test Conference</i> , 6 pp. (1979).

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SIGNATURE: /Phung Chung/

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CONSIDERED: 05/14/2007

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